

Minutes, 5/05/04 Tevatron BPM Upgrade Meeting
Stephen Wolbers

This set of minutes, and all future minutes, are or will be deposited in the Beams Document Database as document number 792.

The agenda as announced consisted of:

1. Report from Steve and Bob.
2. Reports from L2 managers.
3. Report from Dehong on EchoTek testing.
4. AOB.

1. Report from Bob and Steve.

- Bakul and Steve are working on the new baseline of the Tevatron BPM upgrade for the v3.0 of the Run 2 upgrade project. This is due very soon. The main changes are to properly reflect the expected board delivery schedule, the front-end design and engineering, tie the installation to the shutdown and tie the commissioning to the end of the shutdown. In addition we would like to add an integration milestone.

- The first draft MOU for the long-term support of some part of the upgrade will be out soon. It will concern the hardware/electronics.

- Bob will write up his results from using various filters in A1. The conclusion is that the filters we need do have to have good high frequency damping. Along these lines the filter order needs to be written up and submitted very quickly so that we can find out whether there are willing vendors to make them. The specification is finished. We should purchase 1350 filters (9 per diagnostic/front end board times 150 boards).

2. Reports from L2 Managers

Margaret Votava:

- A new version of the Echotek testing plan, AD doc 1114, has been posted.

- Luciano is updating the F.E. software design document reflecting the comments from the review. Then he will be away in Brazil for a couple of weeks (getting married, among other things!)

- There is some worry that 4 modified EchoTek boards that we

receive in June will not be enough for all the testing that the project will want to accomplish. Bob mentioned that I may be requested for the rapid transfer project. We will have to find ways to schedule time/allocate boards, build more test stands as required.

Vince Pavlicek:

- An internal (ESE) review of the timing and diagnostic/front end boards will be held this week.

- Next week there should be a project/external review of the hardware design. After some discussion it was decided that Friday, May 14 at 9-11 A.M. would work. Possible external reviewers include Jim Crisp and Brian Chase.

- Prototype cables, PLL's, etc. are all being acquired for tests.

- The VME crate order has been placed.

Mike Martens:

- Nothing to report this week.

Brian Hendricks:

- Rob Kutschke and Brian met and talked about calibrations and how they would be handled. There is a need for an archival database to keep these time-dependent values.

- Still concerned about diagnostics and how they will be organized. Many issues not yet resolved.

Dehong Zhang:

- The test plan writeup in AD doc 1114 has been updated with the most recent status and results. Vince and Margaret are helping to define and clarify the tasks involved.

- Dehong described some of the demo tests -- count, rawdata, revdata and how they test parts or all of the EchoTek board. There was a question about whether we should use 80 MHz or some other frequency for the clock. Yet to be decided.

Jim Steimel: Technical Coordinator Report

- The calibration specification document will be finished soon.

- The pbar cables are released all around the ring so that work went well. Some of the cable ends look deformed and will need to be tested.

Bob Forster:

- Bob reported that smb connectors will be used for the project. They should work well at 53 MHz. EchoTek has been informed.

4. AOB.