

Notes from June 7, 2004 TeV BPM short-gate sampling mode meeting  
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Most of the relevant information can be found in Jim Steimel's talk.  
Jim's slides can be found in AD doc #1200.

First Jim defined the short-gate sampling mode. Next we had a discussion on how the EchoTek card might be triggered and how it might process under various scenarios. We did not have enough information to decide which of the options is possible, which are possible but difficult, which are easy, etc. Bob pointed out that we do have the ability to get the information we need about the EchoTek board. We do need an expert that can research or knows enough about the EchoTek to answer some of the questions about gating, averaging, etc.

We discussed undersampling in the short-gate sampling mode and how to handle the betatron and synchrotron oscillations.

Next, Jim brought up his current plan for measurements on the teststand.

This includes closed orbit measurements, studying the possibilities and options for running separate channels of the Greychip with different bandwidths, studying operations of the Greychip in a gated mode, and verifying turn by turn and first turn with the new Tev clocks.

Finally, we had a long set of discussions about the timing cards, antiproton measurement techniques and strategy.