Minutes, 8/26/04 Tevatron BPM Upgrade Meeting
Stephen Wolbers

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deposited in the Beams Document Database as document number
792.

The agenda as announced consisted of:

1   General News
2. Bill -- Timing Board status and plans
3. Ken -- Filter Board status and plans
4. AOB

1. General News

   - The Echotek boards are being tested. The single read error was
found to be some sort of interference with other board(s) in the VME
crate. Testing will proceed shortly following the test plan given in
doc #1149.

2. Timing Board status -- Bill Haynes

   - The VME section of the card is functional. The new board has the
same register mapping as the old one. The next step is to get the
interrupts working.

   - Looking at the code that will control the filter board.

   - Two boards are assembled.

   - There are significant changes required for the production board.

   - The plan is to get the board to talk to the filter board and then
to release the board for production.

   - Steve asked how we know that the board works and is what the
project wants/needs. A test procedure is still being developed.

   - Bob/Nathan Eddy asked about the possibility of using this board
for the transfer line BPMs. Nathan will talk to Bill and see whether
the board has all the features required. We can provide the cost/board.

   - Bill's estimate is two more weeks of work before the board can be
released for production.

3. Filter Board status -- Ken Treptow

    - Five pilot filter boards have been assembled. One was used in the A3 test. Some layout changes have been made based on physical placement of components and on pin assignment issues with a filter.

    - A test suite has been written down. Many of the tests have been made, including return loss, insertion loss, cross talk. The remaining tests need to be made and the whole thing documented.

    - Estimate is that within two weeks the boards should be ready for production. There are some long lead time items and we will have to decide which ones we will wait for and which ones we can place on the boards later. The filters should start arriving in bulk the week of Sept 6. We need to decide where the 10 and 20 db attenuators are to go.

4. AOB

    - Eric James asked about Echotek schematics and FPGA design. Mark Bowden will help get these from Echotek.