

Minutes, 12/08/04 Tevatron BPM Upgrade Meeting
Stephen Wolbers

This set of minutes, and all future minutes, are or will be deposited in the Beams Document Database as document number 792.

The agenda as announced consisted of:

1. Report from Bob and Steve
2. Report from Technical Coordinator -- Jim Steimel
3. Report from subproject leaders
4. AOB

1. Report from Bob and Steve.

- The Particle Accelerator Conference abstracts are due Friday December 10. Please submit yours! And add it to the Accelerator Division docDB.

- We will probably be meeting every Wednesday even during the holiday period (watch your mail on Wednesday morning before showing up!). It will be a good day and time to coordinate the activities that will continue through the period. Other meetings (Mondays and Thursdays) will occur as requested/needed).

2. Report from Jim Steimel, Technical Coordinator

- The A3 crate is still in place, though subcomponents have come in and out as debugging required.

- FTP and Snapshot data is working. The results look good so far.

- There are many problems, small and large, that are being debugged. Issues with the diagnostic pages and swapping the system back into normal running mode, inability to do FTP faster than 15 Hz, "phantom" problems that come and go without reason, channels on one Echotek that drop by 10 dB mysteriously, etc.

- Jim may schedule a meeting with a few people to decide how to proceed on a turn-by-turn application.

3. Reports from L2 Managers

Tim Kasza:

- Tim's slides can be found in AD doc 1381-v12.

- Echotek testing continues. Most boards pass but a few have DNL errors and need further investigation/fixes. 5 boards with DNL errors have returned from Echotek and need to be re-tested. Solder bridges were found on various IC pins. We see similar problems on some (all?) of the boards that have DNL problems. Tim asked whether we (Fermilab) should attempt to fix them. Not known yet -- we should look at the 5 that have come back from Echotek. Testing of the 41+ "old firmware" is not yet decided. At least the first few should be upgraded to new firmware, tested and results compared to the original values.

Vince Pavlicek:

- A small number of new timing boards should arrive this week and we can proceed to test them.

- Filter boards are scheduled for production beginning next Monday (Dec 13) but we do not know yet when we will have some here at Fermilab.

- The Optilogic boxes are either ordered or will be ordered.

- Cable panel labels are being made.

- A problem found in the synch lines is being investigated by Bill, Jim and others. One possible fix or workaround is to change the input impedance on the Echotek board to 50 ohms. This is still being investigated.

Rob Kutschke:

- Rob is looking at Tuesday's data and will be posting a note soon about what he has seen. The positions look OK. Rob cannot use pre-shutdown data to do the pbar correction.

- Can go to 1Hz on all BPMs for logging.

4. AOB.