

MI BPM Project
MI BPM TB Control Module Status Report April 11th, 2006, 9:30am

Changes in activities since last status report on April 4th, 2006 are highlighted in yellow.

MI BPM TB Control Module Production

Hardware status:

-) Work in progress on the “Functional Requirements Specification”.
-) Conceptual design of the production version of the Control Module completed.
-) Electrical Schematic completed.
-) Printed Circuit Board Layout work in progress. Component’s footprints completed.
-) Parts ordering completed. Most of the parts have arrived.

Firmware status:

-) Coding work started.

MI BPM TB

Firmware status:

-) Conceptual work in progress together with Control Module (production version) design.

MI BPM TB Control Module Prototype

Hardware status:

-) No activities.

Firmware status (Avnet Xilinx card FPGA):

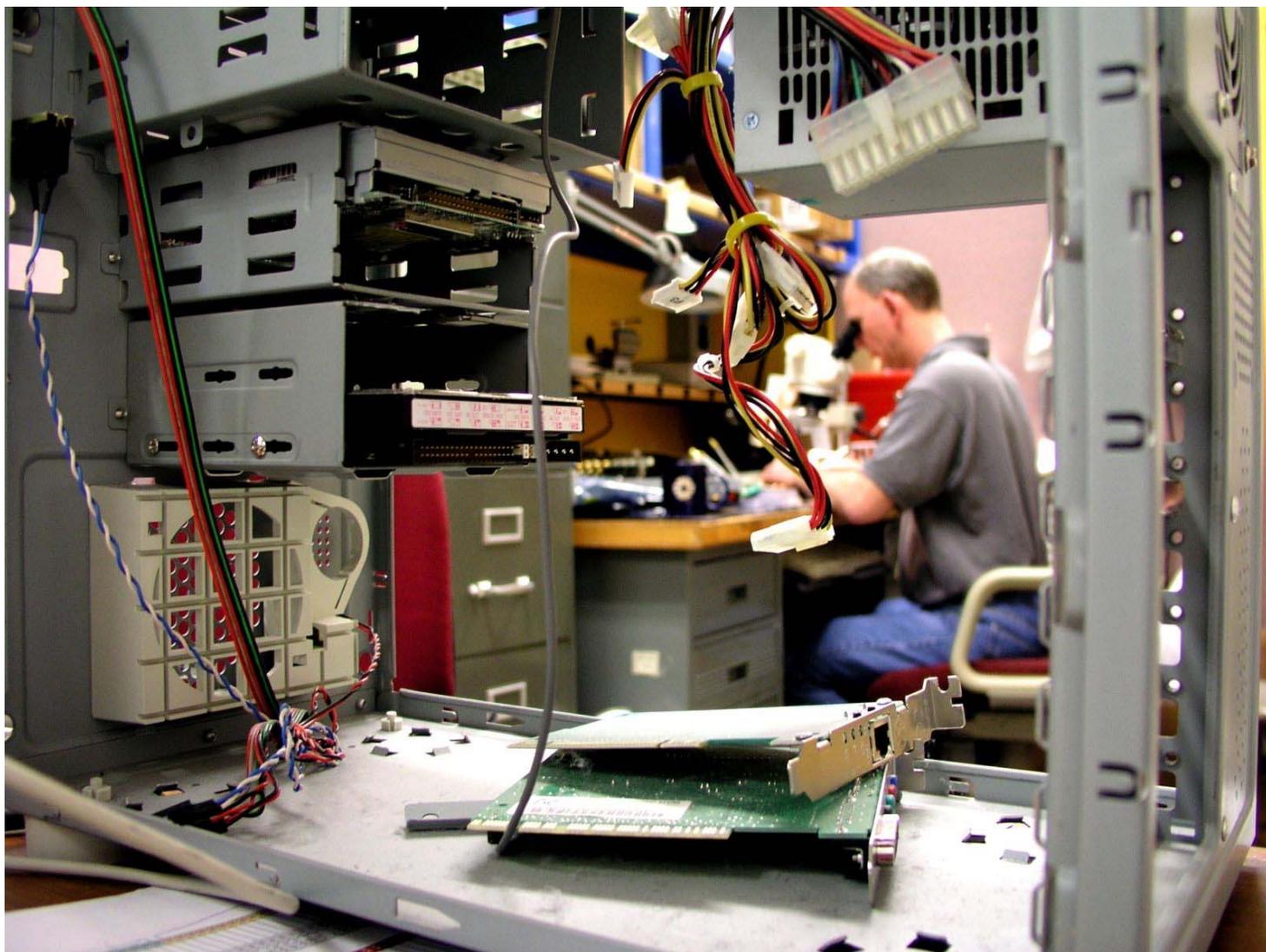
-) No activities.

Document related to the Control Module are available on the web page:

http://www-ese.fnal.gov/MI_BPM_TB_CTL/

MI BPM Project
MI BPM TB Control Module Time Schedule as on February 28th, 2006

Week beginning on	Task 1	Task 2	Task 3	Task 4	Goals and decisions
February 27 th	Design				
March 6 th	Design	Schematic			Finalize design
March 13 th	Firmware	Schematic			
March 20 th	Firmware	Schematic <i>review</i>			Minor design changes
March 27 th	Firmware	Schematic PCB layout		Parts ordering	
April 3 rd	Firmware	Schematic review PCB layout		Parts ordering Parts arrive	
April 10 th	Firmware	PCB layout	PCB quotes	Parts arrive	
April 17 th	Firmware	PCB manufacturing	Front panel design	Parts arrive	Finalize preliminary version of firmware.
April 24 th		Module assembly	Front panel manufacturing		1 st Module assembled and tested
May 1 st		Module assembly	Front panel manufacturing		May 1 st : Does module meets system requirements?
May 8 th		Module testing	Front panel manufacturing		
May 15 th					May 15 th : All Modules assembled and tested
May 22 nd					
May 29 th					



April 10, 2006. Neal Wilcer working ~~at~~^{on} his computer