

**MI BPM Project**  
**MI BPM TB Control Module Status Report May 30<sup>th</sup>, 2006**

Significant changes are highlighted.

**MI BPM TB Control Module Production**

**Hardware status:**

- ) Preliminary Manual for the Control Module is available.
- ) Almost 12 Modules assembled and tested.
- ) Remaining 4 modules assembly/testing in progress, almost completed.

**Firmware status:**

- ) Coding work in progress.

**MI BPM TB**

**Firmware status:**

- ) Conceptual work in progress together with Control Module (production version) design.

**Hardware:**

- ) Assembled Power cable harness for Analog crate. Thanks to Tim Kasza and Bill Barker.  
Load test to be performed.
- ) Ordered components for power cable harness.
  
- ) More tests to be done at FCC  
(10 “production” TBs, 1 prototype TB and 1 Control Module)
  - a) Backplane termination scheme, 53 MHz test signal quality
  - b) Power supplies load test.
  - c) Software control/interface.
  - d) Full System test Timing Module > Analog Crate > Echotek

**MI BPM TB Control Module Prototype**

**Hardware status:**

- ) No activities.

**Firmware status (Avnet Xilinx card FPGA):**

- ) No activities.

Document related to the Control Module are available on the web page:

[http://www-ese.fnal.gov/MI\\_BPM\\_TB\\_CTL/](http://www-ese.fnal.gov/MI_BPM_TB_CTL/)

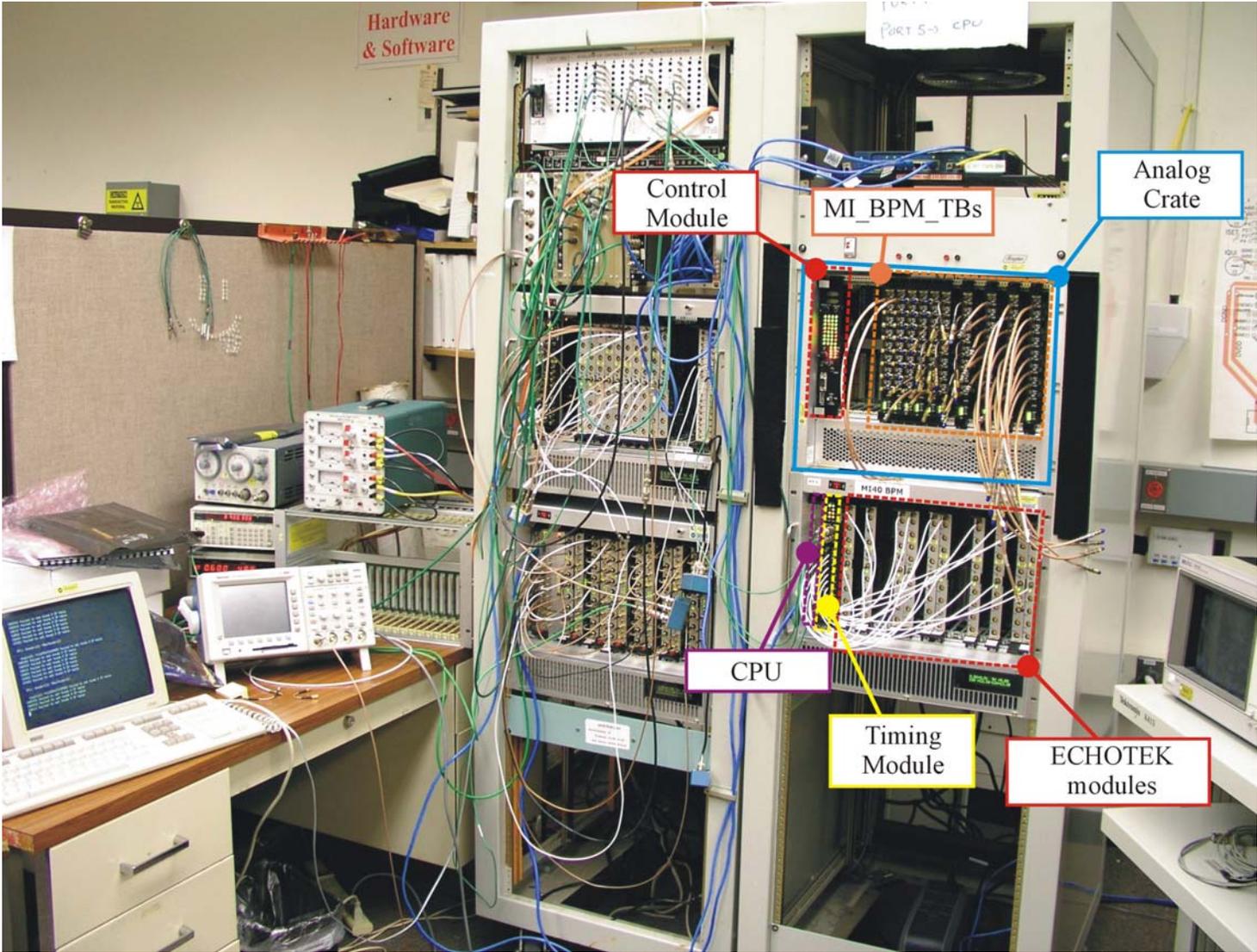
**MI BPM Project**  
**MI BPM TB Control Module Time Schedule**  
 As on February 28<sup>th</sup>, 2006, last modified on May 30<sup>th</sup>, 2006

New changes in schedule are **highlighted**. Previous changes in schedule are **in blue**.

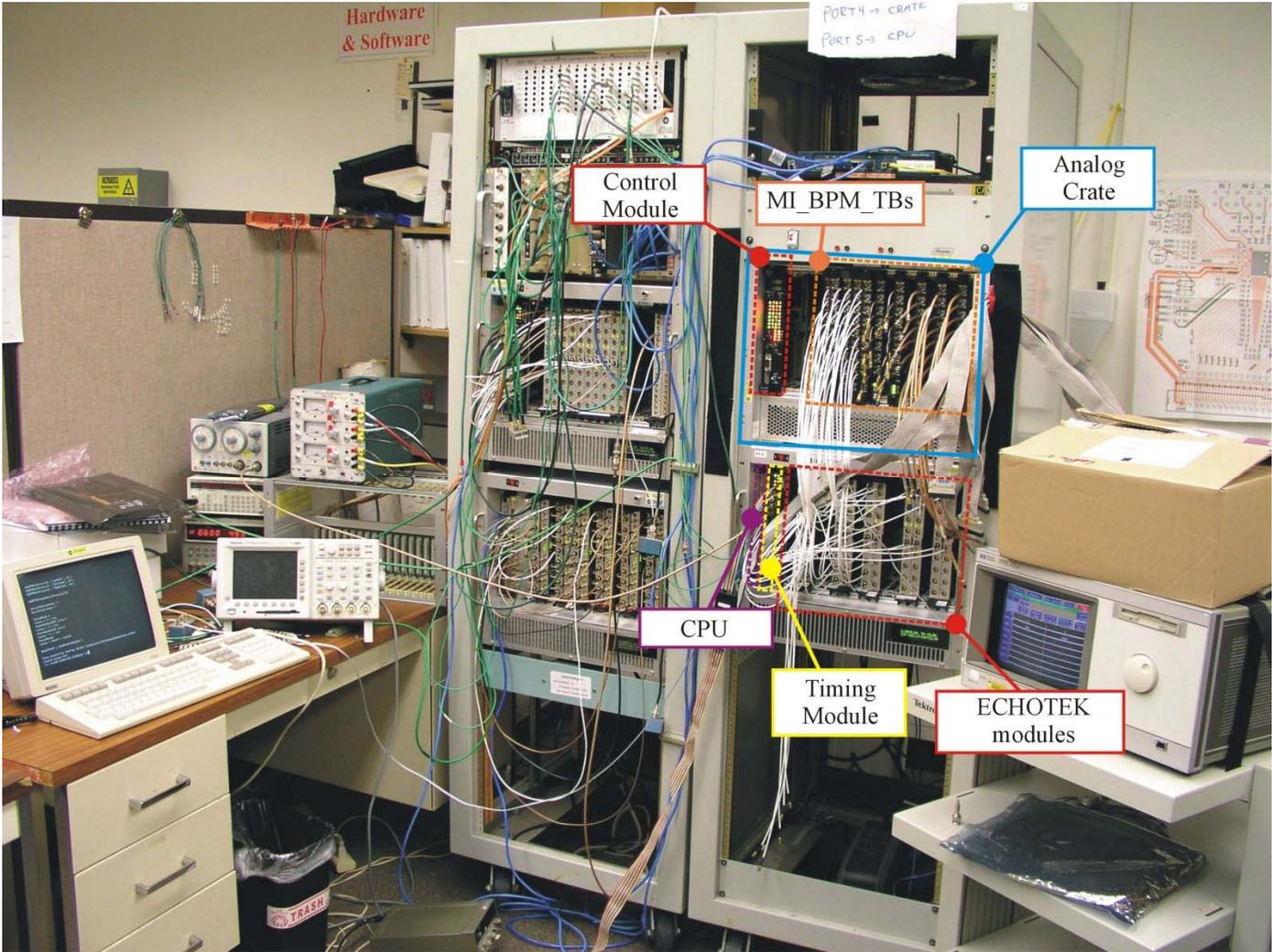
Week beginning on	Task 1	Task 2	Task 3	Task 4	Goals and decisions
February 27 <sup>th</sup>	Design				
March 6 <sup>th</sup>	Design	Schematic			Finalize design
March 13 <sup>th</sup>	Firmware	Schematic			
March 20 <sup>th</sup>	Firmware	Schematic <b>review</b>			Minor design changes
March 27 <sup>th</sup>	Firmware	<b>Schematic</b> <b>PCB layout</b>		Parts ordering	
April 3 <sup>rd</sup>	Firmware	<b>Schematic review</b> <b>PCB layout</b>		<b>Parts ordering</b> <b>Parts arrive</b>	
April 10 <sup>th</sup>	Firmware	PCB layout	<b>PCB quotes</b>	Parts arrive	
April 17 <sup>th</sup>	Firmware	PCB layout <b>PCB</b> <b>manufacturing</b>	PCB quotes <b>Front panel</b> <b>design</b>	Parts arrive	<b>Finalize preliminary version of firmware.</b>
April 24 <sup>th</sup>	<b>Firmware</b>	<b>PCB</b> <b>Manufacturing</b> Module assembly	<b>Front panel</b> <b>manufacturing</b> <b>Front panel</b> <b>design</b>		Finalize preliminary version of firmware. <b>1<sup>st</sup> Module assembled and tested</b>
May 1 <sup>st</sup>		Module assembly	Front panel manufacturing		<b>1<sup>st</sup> Module assembled and tested</b> May 1 <sup>st</sup> : Does module meets system requirements?
May 8 <sup>th</sup>		<b>Module assembly/</b> <b>Module testing</b>	Front panel manufacturing		
May 15 <sup>th</sup>		<b>Module assembly/</b> <b>Module testing</b>			May 15 <sup>th</sup> : All Modules assembled and tested
May 22 <sup>nd</sup>		<b>Module assembly/</b> <b>Module testing</b>			
May 29 <sup>th</sup>		<b>Module assembly/</b> <b>Module testing</b>			

Module	0	1	2	3	4	5	6	7	8	9	10	11
100%												
90%												
80%												
70%												
60%												
50%												
40%												
30%												
20%												
10%												

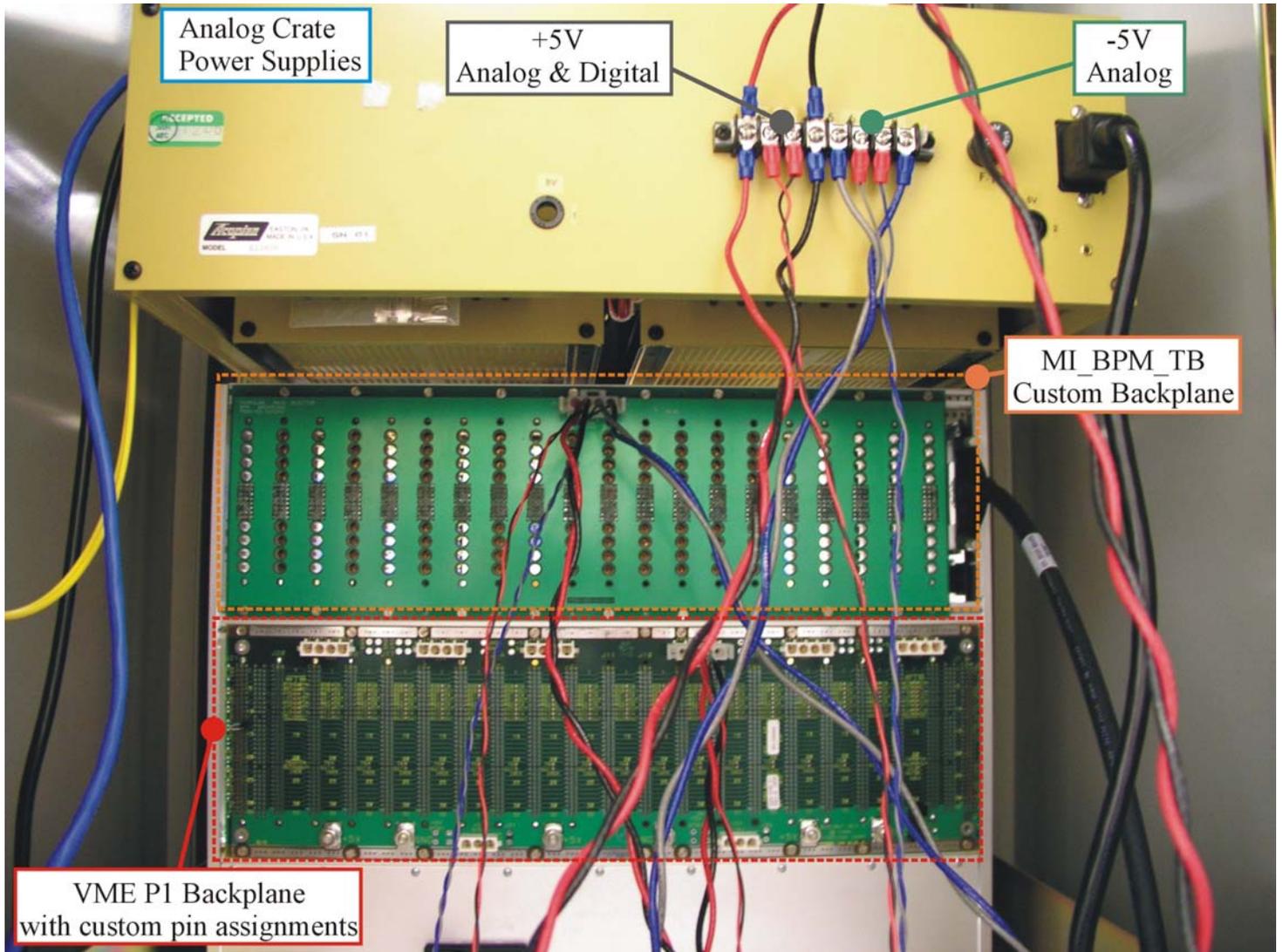
Module Assembly status



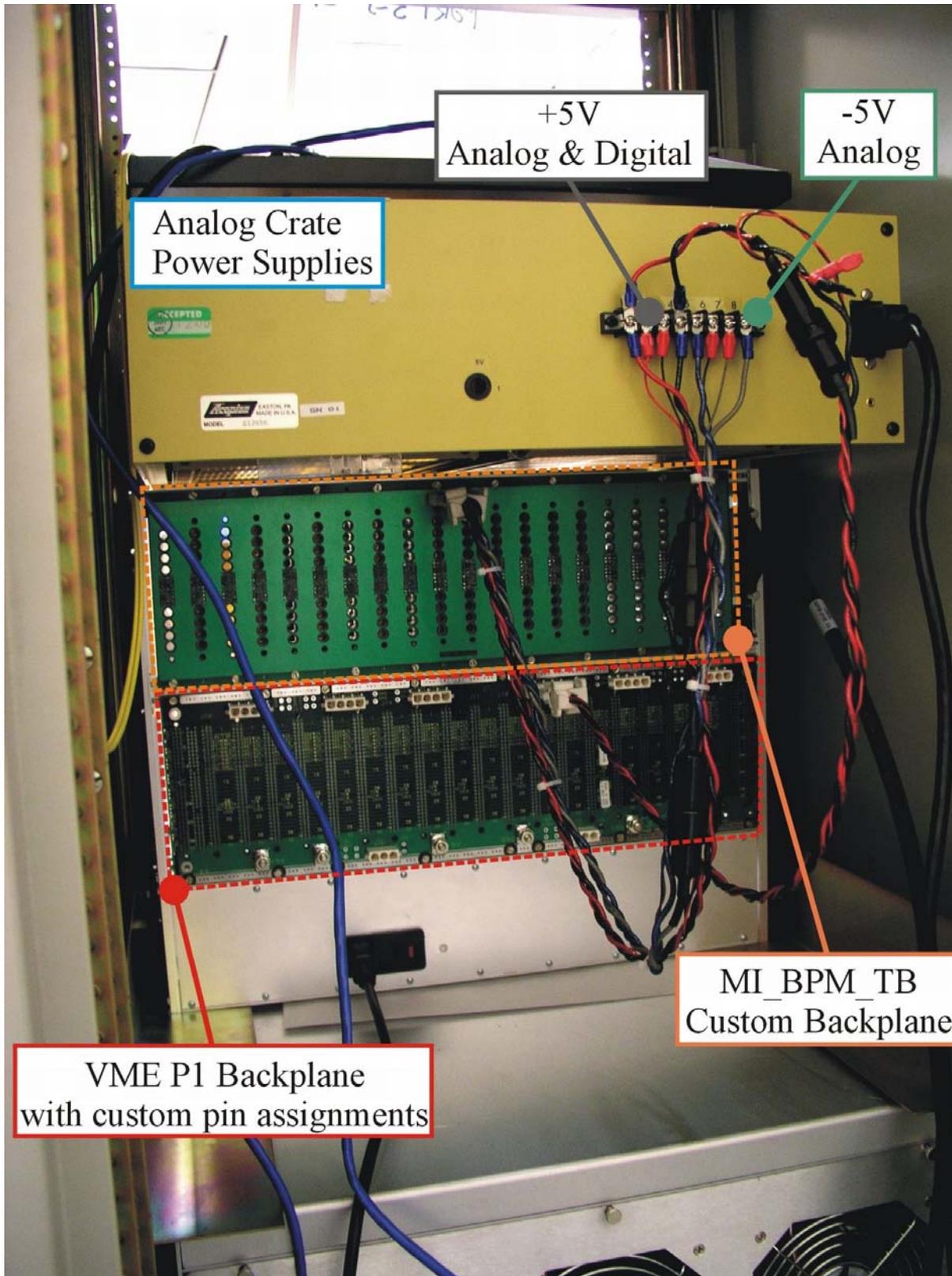
FCC MI BPM Test Area, May 23, 2006



FCC MI BPM Test Area, May 30, 2006



FCC MI BPM Test Area, Analog Crate Power Supplies and Backplanes, May 23, 2006



FCC MI BPM Test Area, Analog Crate Power Supplies and Backplanes, May 30, 2006



FCC MI BPM Test Area, Digital Cable Fuse, May 30, 2006