Mathematical Description of the Booster LLRF Controls

Early Draft Version January 7, 2008

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January 7, 2008

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# Introduction

This is an early draft which collects the gains and transfer functions of the electronic modules that implement the Booster LLRF control. Future additions to this document will include a representation of the Booster beam dynamics. This will describe the relationships between jRF voltage and phase, bending magnet magnetic field, beam momentum, and beam radial position.

The end goal is to understand and model the Booster and the LLRF controls sufficiently that modifications can be made to the LLRF controls with confidence that we will have not only a working system when we are done, but also and improved system.

# The Acceleration Phase Lock Loop

The Acceleration Phase Lock Loop measures the phase difference between the Booster beam bunch phase and the phase of the LLRF Digital Frequency Source (DFS) VXI module. The phase difference adjusts the DFS frequency output to regulate this phase difference. A block diagram of this phase-lock-loop is given in Figure II.1

## II.1 The Phase Detector

The Fast Phase Detector provides a voltage output proportional to the phase difference between the RF sine wave voltage generated from the beam pickup and the “delayed” LLRF reference sine wave. The output is {-10 to +10 Volts} ⬄ {0, 180 degrees}. This give us the following:

Eq II.1

The phase detector has a -3dB low-pass roll off at 1 MHz. This gives the full phase detector transfer function as:

Eq II.2

Neglecting the 1 MHz roll off we get:

Eq II.3

## II.2 The Loop Filter

The PLL loop filter used is a Lag-Lead type. Circuit analysis of the Detector Mode Control module results in the following transfer function between the input and output voltages.

Eq II.4

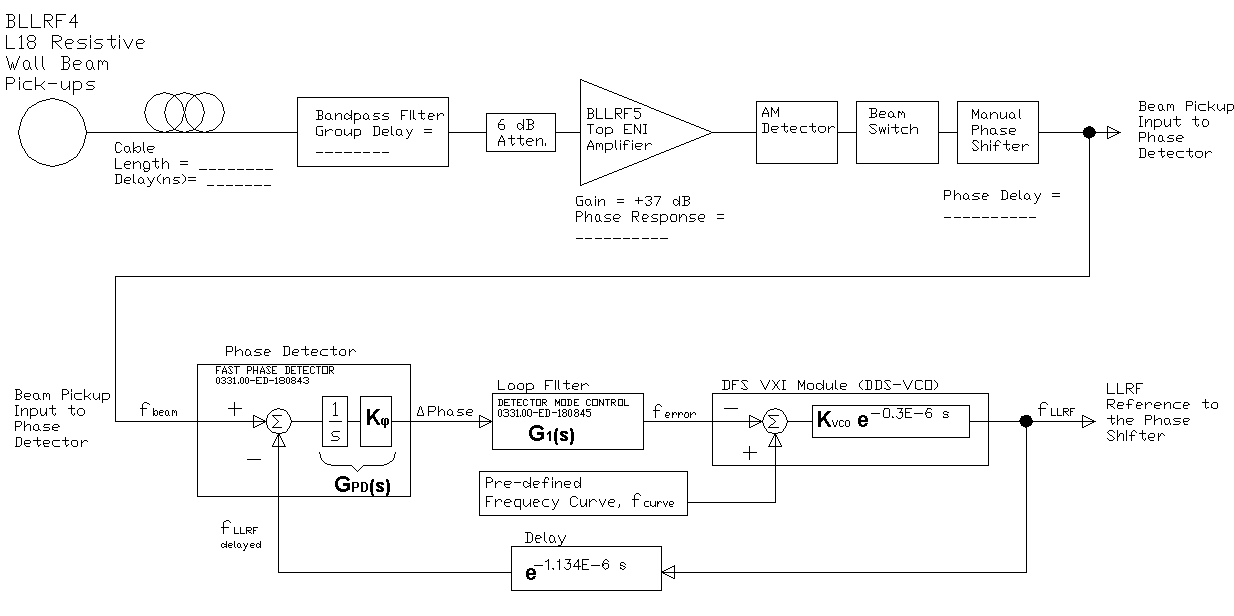


Figure II.1 Block Diagram of the Acceleration Phase Lock Loop.

## II.3 The Digital Frequency Source VXI Module

The Digital Frequency Source (DFS) provides the LLRF reference sine wave that drives the voltage in the Booster accelerating cavities. The signal is produced digitally using a Stanford Telecom, STEL-2273A, direct digital synthesizer board (DDS). This is mainly a high speed, 8 bit DAC driven by a numerically controlled oscillator ASIC. The output of the DDS is controlled by writing Frequency Control Words from the DSP processor on the VXI module. The desired frequency is determined by a pre-defined frequency curve plus the error correction from the phase detector. The phase detector voltage is digitized by an Analog Devices AD872A, 12-Bit, 10 MSPS A/D Converter. A more detailed block diagram of the DFS VXI Module is given in Figure II.3.2.

From the information in Figure II.3.2 we can derive the overall gain and delay of the DFS.

Eq II.5

Eq II.6

## II.4 The Acceleration Phase Loop Analysis

### II.4.1 Analysis as a Sampled Data System

The phase error input to the DFS VXI module is digitized every 0.1 µs. The output frequency is updated every 1.0 µs. These two sampling instances are not synchronized in this module, but with good approximation we should be able to analyze this control loop as a “sampled data system” with a sampling period of 1.0 µs. A sampled data system is one having both continuous signals and discrete, sampled, signals.

The analysis of sampled data systems is covered in nearly every text with a chapter on digital control [1], [2]. The analysis allows the designed to arrive at a discrete-time transfer function with which digital control methods and analyses can be applied. However given the location of the sampler in this system we are not able to arrive at a discrete-time transfer function. This situation is described in the textbooks.

*Just for the academics the analysis is worked through as far as it will go. You will not miss much if you skip to the next section.*

The block diagram for our sampled data system is given in Figure II.4.1.1. Here we include the sampling switch and the contribution to the transfer function of the DFS by the addition of a sample and hold.

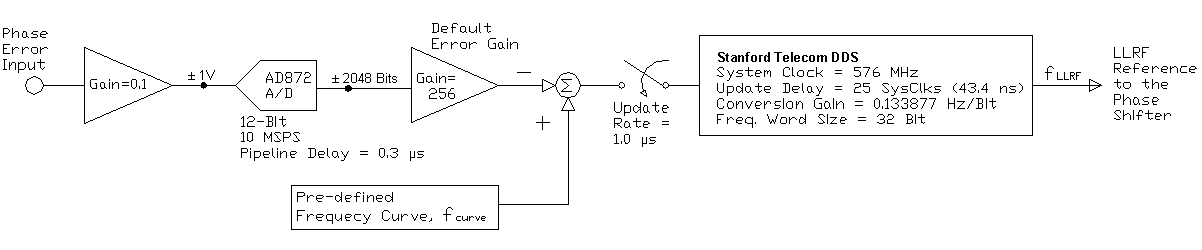


Figure II.3.2 Block diagram of the details of the DFS VXI module.

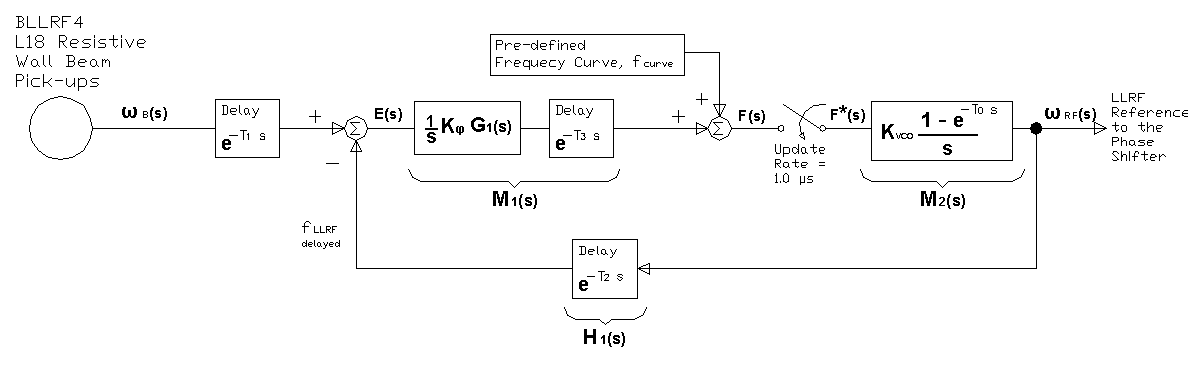


Figure II.4.1.1 Block diagram of the Acceleration Phase Control Loop as a sampled data system.

We define our continuous signals from the block diagram.

Going to the sampled version of the signals we have

Solve for .

Substitute into the equation for .

We would want to have a transfer function in the form of . However cannot be separated out in the equation for above. Oh what fun we could have had if we could have gotten a discrete transfer function. The explanation as to why we cannot get what we want is best stated by [1].

This system displays an important fact that all our facile manipulations of samples may cause us to neglect: a sampled data system is *time varying*. The response depends on the time *relative to the sampling instant* at which the signal is applied. Only when the input samples alone are required to generate the output samples can we obtain a transfer function.

The problem with our configuration is that the output does not depend on the values of at the sampling instance, but it depends on the value of at the sampling instance AND previous values of through ().

### II.4.2 Analysis as a Continuous Time System

With little justification other than to say that the 1 MHz sampling rate is more than 100 times the highest frequency component of our phase error signal, I will proceed to analyze the system as if it were not sampled.

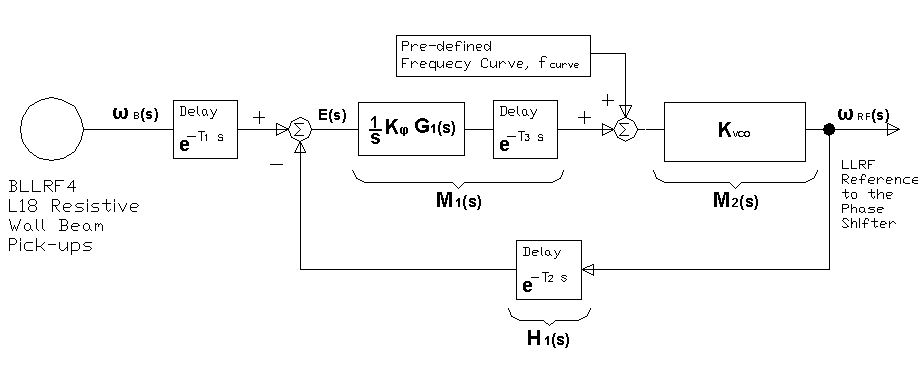


Figure II.4.2.1 The continuous time system block diagram.

Given the block diagram in Figure II.4.2.1 our transfer function between the beam frequency at the pickup and the output of the DFS is

We will use a (1,1) Pade’ approximation for the delay terms.

Let , where

, where

And also let

Where,

|  |
| --- |
|  |
|  |

Recall for the existing system

,

# Radial Position Control Loop

## III.1 Radial Position Control Transfer Functions

The Radial Position Controller, also called the LLRF Phase Controller (Dwg# 0331.00-ED-63397), compares a Beam Position Monitor (BPM) measurement to a signal representing the desired radial position offset. From the difference or error between these two signals a phase shift in the LLRF reference to the RF cavities is determined. As will be described later, this phase shift will result in regulating the beam radial position. A block diagram of the Radial Position Controller is given in Figure III.1.1 and we will use this to describe the transfer function of the controller.

The radius of the Booster is 74.47 Meters. Marker: Check on actual effective Radius. The center of the BPM is assumed to sit at this radius, and the +/- position of the beam measured by the BPM is actually the radial offset of the beam from the Booster radius. This signal is referred to as RPOS in the documentation and in the ACNET control system. It is a positive signal for radiuses larger than the Booster radius and negative for radiuses smaller. The radial offset signal, ROFFF, has the opposite polarity. ROFFF is a curve that is defined by the physicists tuning the Booster, varying through the acceleration cycle.

Besides the error between RPOS and ROFFF, there are other system variables that drive the phase shift of the Low Level RF. Get description from Bill

The transfer functions and gains listed in the block diagram of Figure III.1.1 are the following

Letting , the transfer function between RPERR and the PSDRV output is

**Also describe the Phase Shifter Module here Gain=9 degrees/Volt**

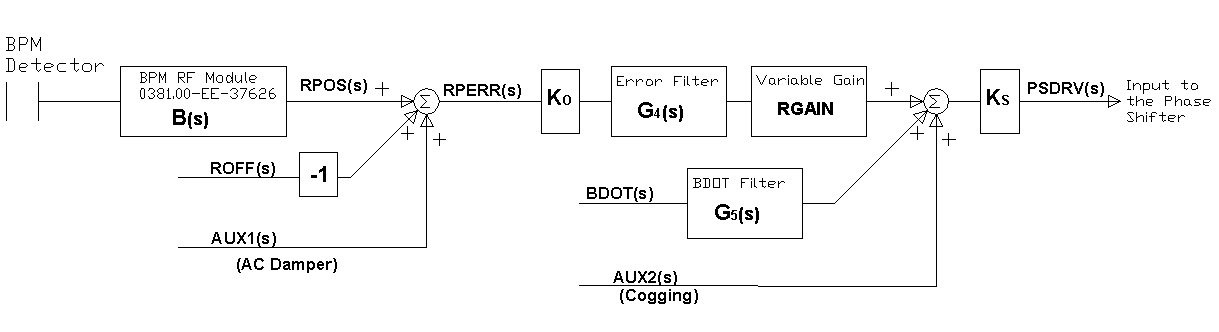


Figure III.1.1 Block Diagram of the Radial Position Controller (LLRF Phase Controller, 0331.00-ED-63397).

## III.2 Radial Position Control State Variable Representation

As we advance with our modeling and analysis of our control system, we will find benefit in describing our systems using a state variable representation. Transfer functions are quite useful in describing the dynamics of the electronics since these are linear time-invariant circuits (with the exception of RGAIN which in reality is implemented as a time varying curve). However, once we begin to consider the accelerator beam dynamics we will want a representation of our dynamics that can model linear and non-linear systems, time-invariant and time varying, single variable and multi-variable systems in a unified manner. This is where linear algebra and a state variable representation of our system can help.

All modern controls text cover at least the basics of state variable analysis [2],[3]. To transform our set of transfer functions for the Radial Position Control into state variable form we will use a “Direct Decomposition” method using a signal flow graph to help visualize the transformation.

We start with the relationship for the RPERR input.

We will alter the transfer function so that it only has negative powers of , and then multiply the numerator and denominator by a dummy variable .

Using our dummy variable we can represent the numerator and the denominator separately.

and

or

Figure III.2 gives the state diagram for these equations, [2].

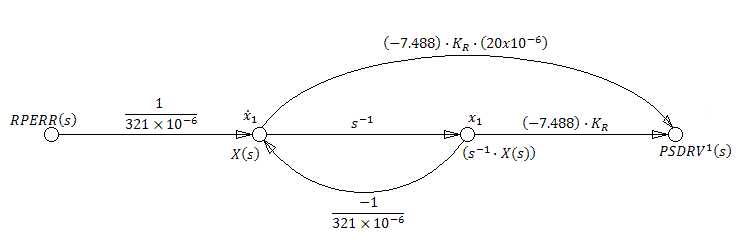


Figure III.2 State Diagram of the transfer function between RPERR and PSDRV1 .

From this state diagram we can assign time variables to the nodes giving us the following state and output equations.

Simplifying these equations we get

Similarly we can find state variable expressions for the other transfer functions.

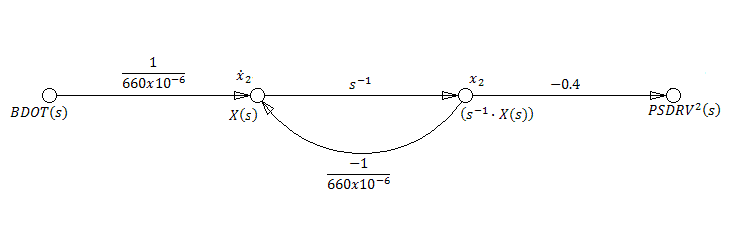


Figure III.3 State diagram of the transfer function between BDOT and PSDRV2 .

The final expression of our state variable equations in matrix form is

# qVxB figure.GIFAPPENDIX A: Units for Momentum, Magnetic Field and Radius

# APPENDIX B: Computing the Expected Magnetic Field

Here we will make a comparison between what we ideally expect the magnetic field in the Booster bending magnets will be, versus B(t) derived from a measurement of dB(t)/dt and a set of assumptions about the measurement.

First recall from Appendix A our equation describing the relationship between momentum (P), the radius of orbit of the protons (R), and the bending magnetic field.

We also know that B(t) should have the following form.

Where

We can let R simply be the radius of the Booster and P(t=0 s) and P(t=33.3 ms) be determined by the expected beam energy injection and extraction, respectively.

For

We get

And then

The question now is, how do we convert the measured voltage representing dB(t)/dt to arrive at a measured value of B(t). From TM-405, “Booster Synchrotron” 1976 design report, we get that the injection current is 74 Amps for an injection energy of 200 MeV. The momentum in this case is 644.446 MeV/c, and from our equation with R=74.47 Meters, B(0 s) is 0.0289 Tesla. Our conversion between Amps through the windings and the resulting magnetic field is

PICK IT UP HERE: DECIDE WHAT WE WANT TO REFERENCE TO GET AT THE TRUE Imin and Imax ?

# REFERENCES

1. G. F. Franklin, J. D. Powell, “Digital Control of Dynamic Systems,” Reading, MA: Addison-Wesley, 1980, TJ216.F72.
2. B. C. Kuo, “Automatic Control Systems,” 5th ed., Englewood Cliffs, NJ, 1987, TJ213.K8354.
3. G. F. Franklin, J. D. Powell, “Feedback Control of Dynamic Systems,” 3rd ed., Reading, MA: Addison-Wesley, 1994, TJ216.F723.