

# VRFT Upgrade Status

- Circuit board layout is nearly finished. Several preliminary routes have been made followed by adjustments to routing parameters. OrCad auto route completes in less than an hour but 1-2 days of cleanup are required post routing.
- Circuit board manufacturer has purchase order in hand; waiting for design files to be delivered – target date Friday 11/14. Delivery of boards 10 business days after receipt of design files.
- I need to finish ordering components; should be received by the time circuit boards are delivered.
- FPGA designs will be reviewed, simulated and qualified or revised as needed while waiting for circuit boards and components to be delivered.
- Estimate two weeks to assemble first board; one to two weeks to test.