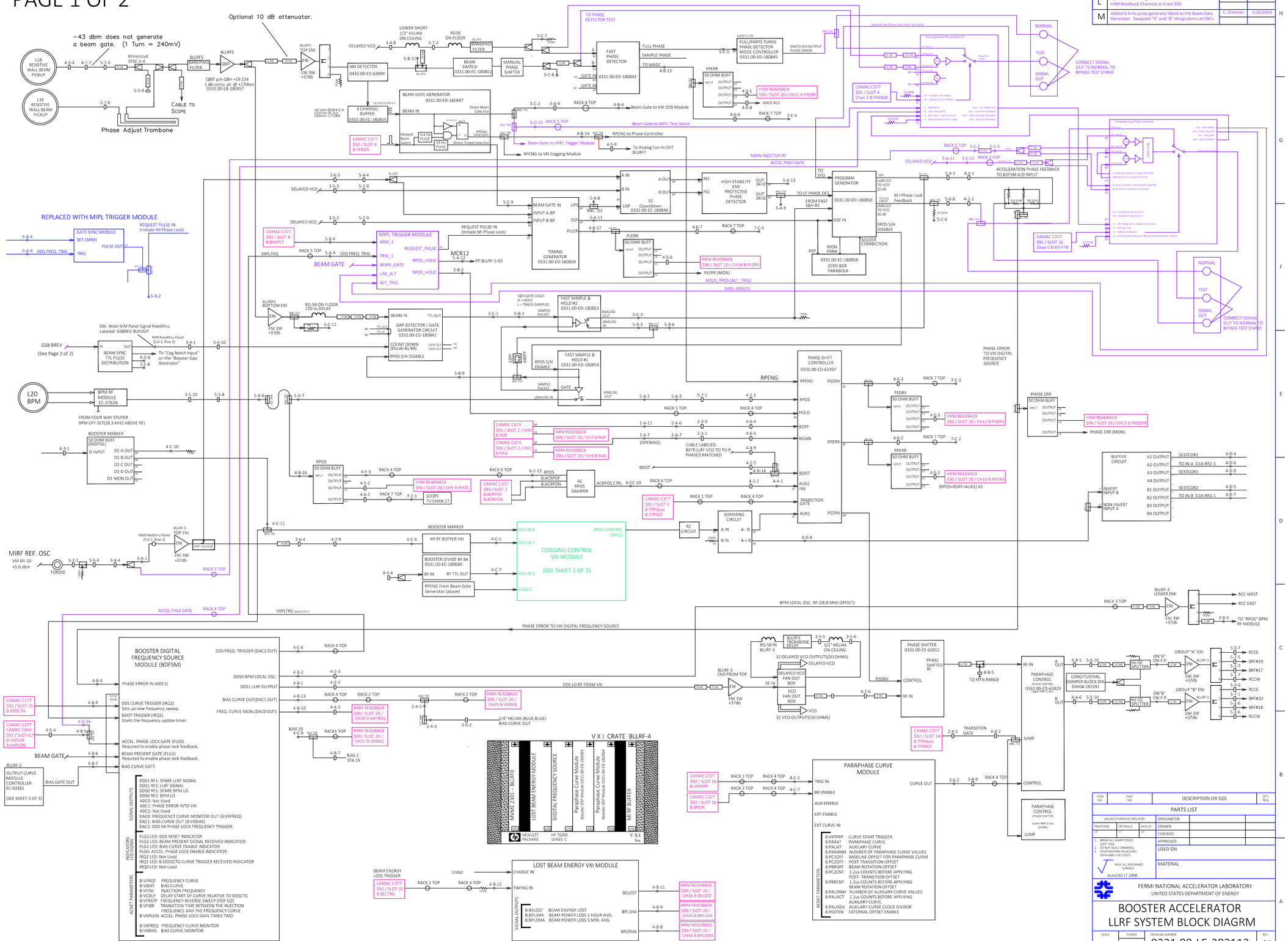
BOOSTER ACCELERATOR LLRF SYSTEM BLOCK DIAGRAM 02/20/2014
PAGE 1 OF 2

REV.	DESCRIPTION	DRAWN / APPD.	DATE / DATE
K			
L	Updated Block Diagram to include corrections and new HRM Readback Channels in Crate $90	C. Drennan	9/26/2013
M	Added 0.4 ms pulse generator block to the Beam Gate Generator. Swapped "A" and "B" designations at ENI's.	C. Drennan	2/20/2014



ITEM NO.	PART NO.	DESCRIPTION OR SIZE	QTY. REQ.

PARTS LIST

UNLESS OTHERWISE SPECIFIED: FRACTIONS ±/- DECIMALS ±/- ANGLES ±/°

1. BREAK ALL SHARP EDGES 1/64" MAX.
2. DO NOT SCALE DRAWING.
3. DIMENSIONING IN ACCORD WITH ANSI Y14.5 STD'S.

MAX. ALL MACHINED SURFACE

AutoCAD LT 2008

ORIGINATOR / DRAWN / CHECKED / APPROVED / USED ON / MATERIAL

FERMI NATIONAL ACCELERATOR LABORATORY
UNITED STATES DEPARTMENT OF ENERGY

BOOSTER ACCELERATOR
LLRF SYSTEM DIAGRAM

SCALE / FILMED / DRAWING NUMBER 0331.00-LE-282113 / REV. M