Notes from 11/13/03 Tev BPM Hardware discussion.
These are kept in BD Doc 906.

Present: Jim Steimel, Vince Pavlicek, Luciano Piccoli,
Dehong Zhang, Brian Chase, Mike Martens, Stephen Wolbers, Rob Kutschke, Bob
Webber, Mark Bowden

Main Topics: Echotek news, DSR board teststand show and tell, issues of data buffers.

Echotek:

  - No new news from Echotek about the proposed "modified RR" board. We might hear
something on Monday.

Data:

  - We had quite a long discussion about data flow in the system, keying off the diagram
that Jim Steimel has produced and deposited as BD Doc 903. It was a good discussion
and helped clear up some issues about the functionality requirements of the system and
how various hardware solutions could meet them.

RR Echotek:

  - Bob Webber is looking at how the current, unchanged RR Echotek board might or
might not be able to meet the requirements for the Tevatron BPM readout.

Action Items for the Next Meeting:

  - Mark Bowden will try to get more information from Echotek about cost and schedule.

  - Jim Steimel will talk about schedule, effort and cost and performance characteristics
of the systems being considered.

  - Vince Pavlicek is working on the cost/schedule information for the damper board and
this information will be fed to Jim Steimel for integration into the overall comparisons.

DSR Board show and tell:

  - Many of us went to see the teststand in Brian Chase's lab area of a Tevatron BPM
with a wire running through it, all 4 outputs fed into a DSR board, oscilloscope and
ACNET/controls readout. It was a nice setup and when the wire moved, the output
signals moved!

We will next meet on Monday, November 17, 11:00 A.M., Penthouse.