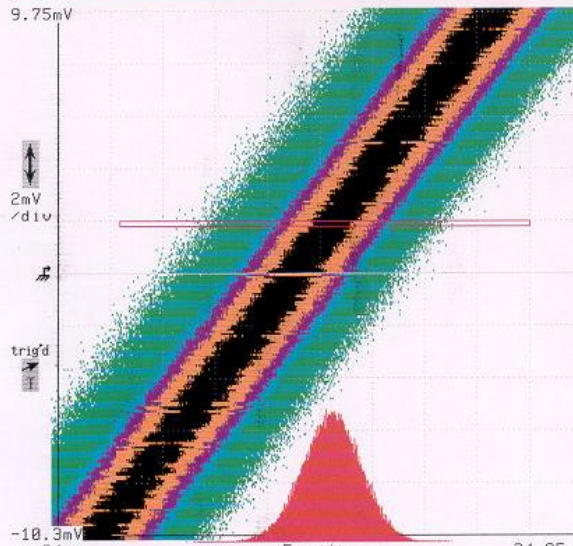


Accelerator Clock Jitter
Measurement

Conner Winfield
Free Running

11801B DIGITAL SAMPLING OSCILLOSCOPE
date: 16-MAR-22 time: 14:06:52

Hardcopy complete



Top	1.75mV	Mean	34.03ns	wt1σ	69.781%	Main Size	
Btm	1.55mV	RMSΔ	2.895ps	wt2σ	95.547%	Sps/div	
Lft	34.01ns	PkPk	25.4ps	wt3σ	99.628%	Main Pos	
Rgt	34.05ns	Hits	155501	Wfms	26115	34.004ns	
Persist	Mask	Color	Grad	Standard	Remove/Clr		
Histograms	Testing	Scale		Masks	Trace 1		
Color Grad	Count Off			User Mask	M3		
Continuous					Main		

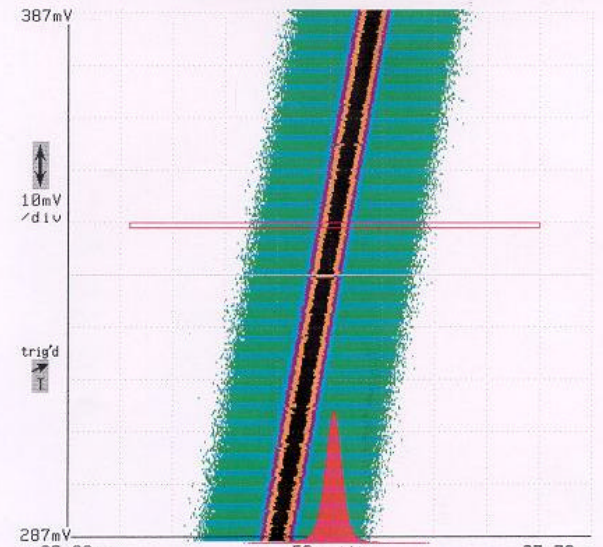
Conner Winfield Jitter
Free Running

Conner Winfield
input clock jitter

11801B DIGITAL SAMPLING OSCILLOSCOPE
date: 17-MAR-22 time: 17:31:53

Tek

← L Cursors Window FFTmag Def Tra

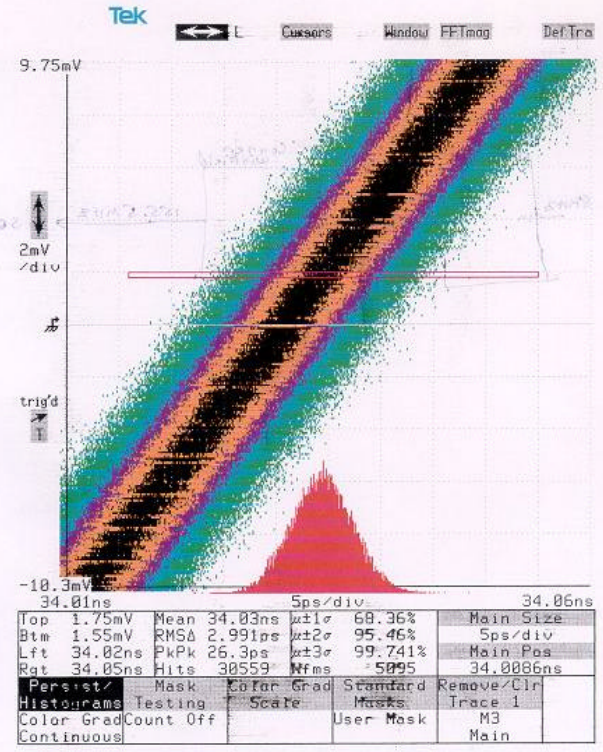


Top	347mV	Mean	27.54ns	$\mu 1\sigma$	82.797%	Main Size
Btm	346mV	RMS Δ	13.6ps	$\mu 2\sigma$	94.668%	50ps/div
Lft	27.34ns	PKPk	177ps	$\mu 3\sigma$	97.499%	Main Pos
Ret	27.73ns	Hits	184464	Wfms	129194	27.27564ns
Persist/	Mask	Color Grad	Standard	Remove/Cir		
Histograms	Testing	Scale	Masks	Trace 1		
Color Grad	Count Off		User Mask	M3		
Continuous				Main		

Conner Winfield
Input Clock Jitter

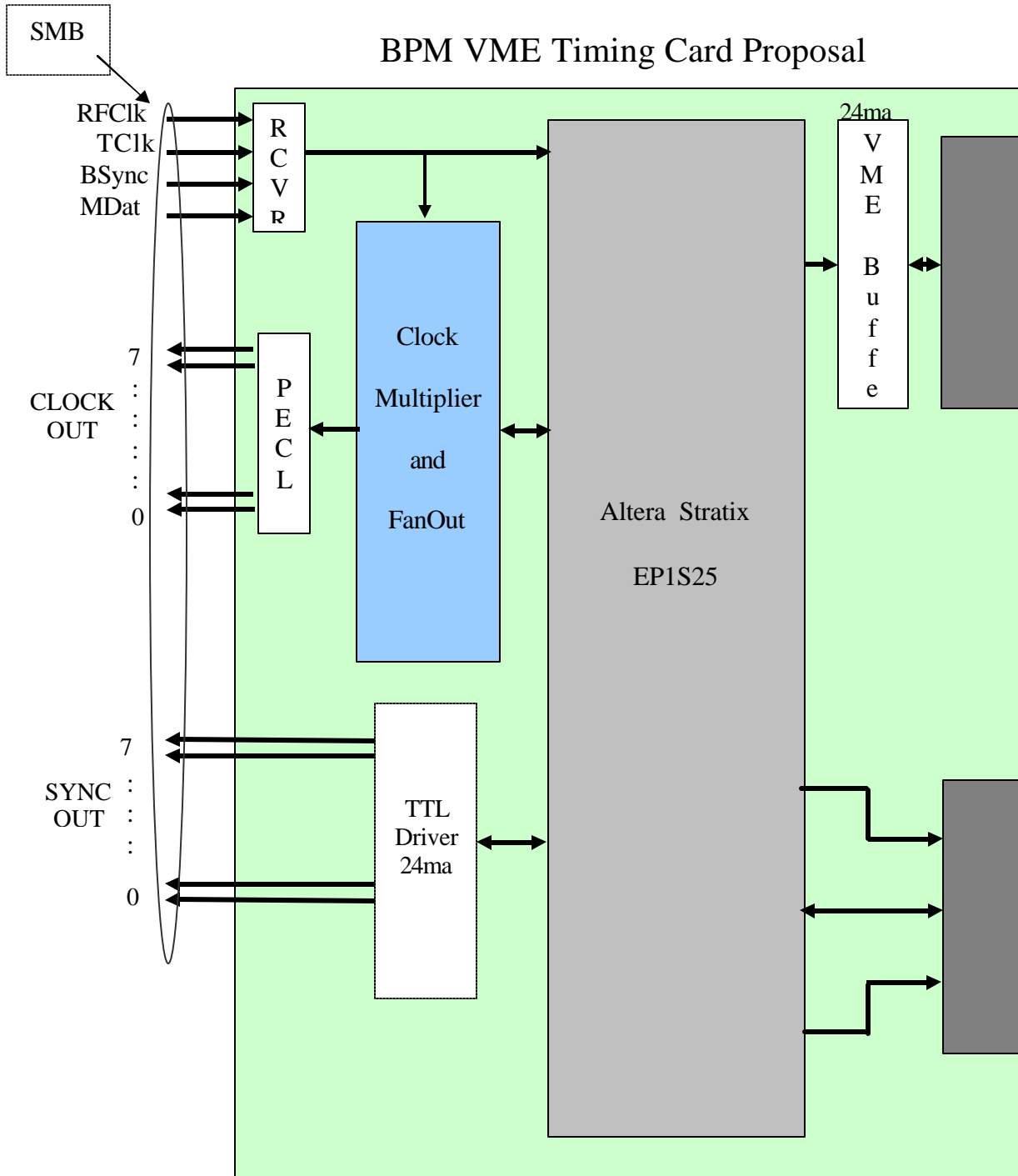
Conner Winfield
 output = 15.55 MHz
 locked to 57.84 MHz

11801B DIGITAL SAMPLING OSCILLOSCOPE
 date: 16-MAR-22 time: 11:30:10

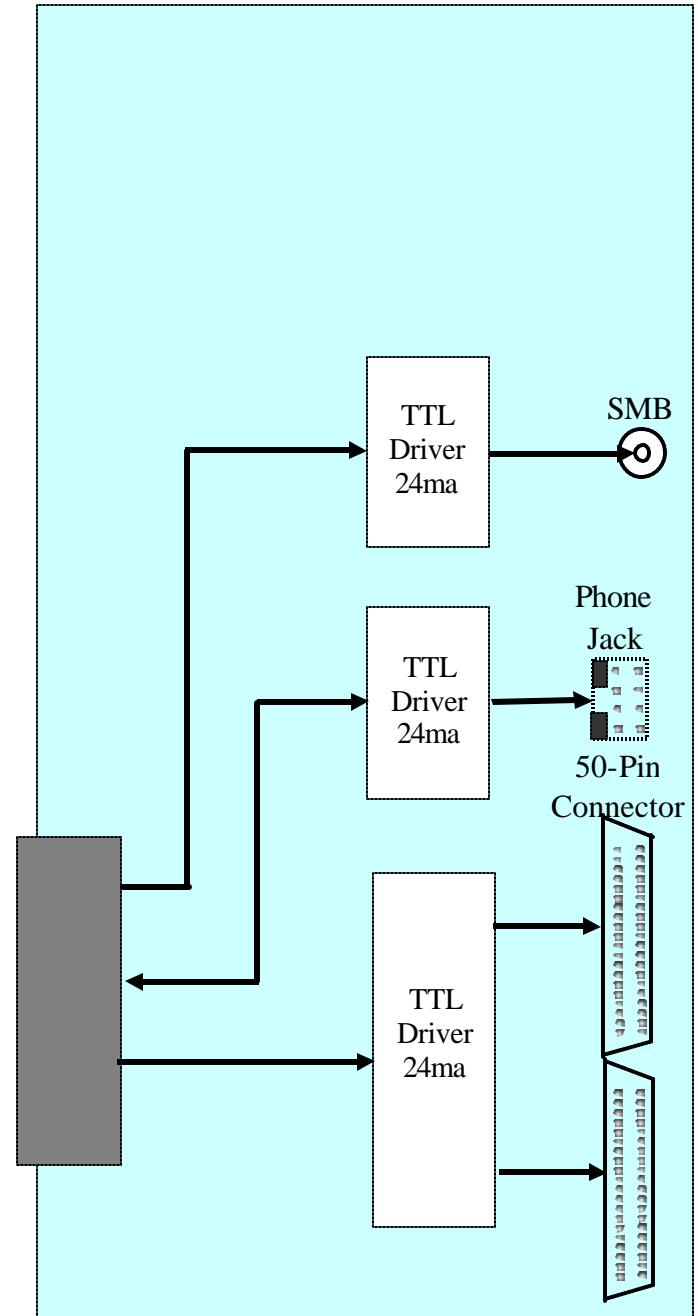


Conner Winfield
 Output Clock Jitter
 Locked to Input

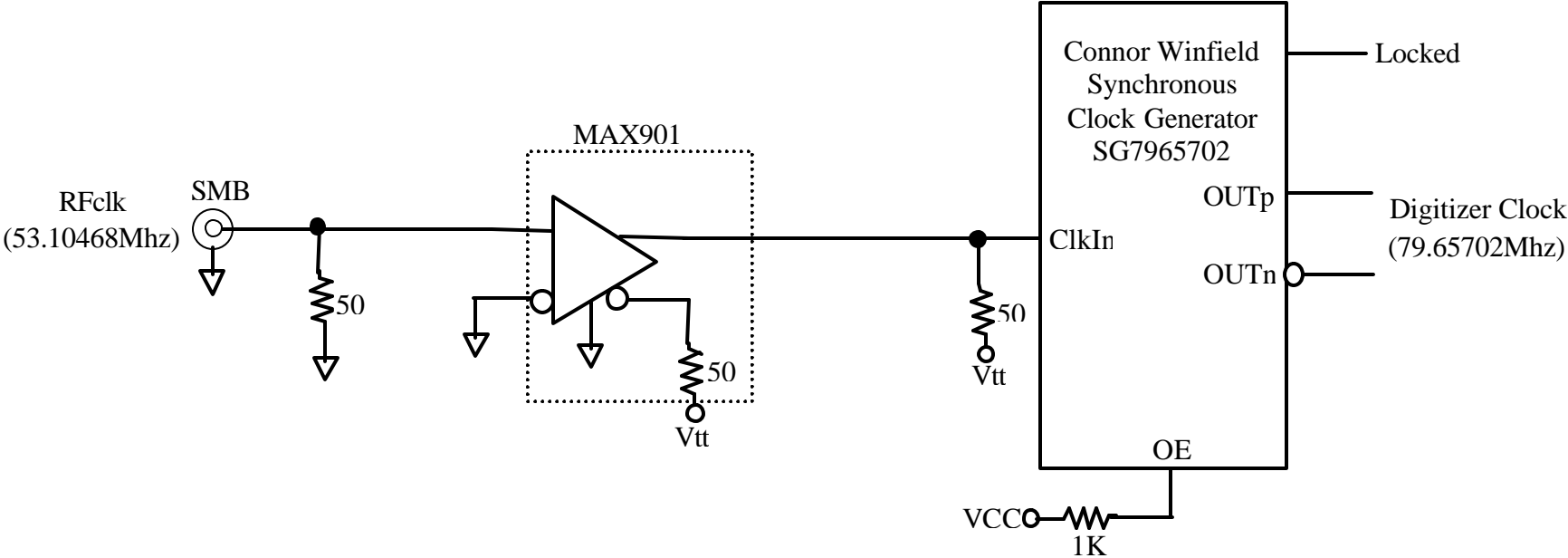
BPM VME Timing Card Proposal



VME Transition Card



Clock Generation (79Mhz)



Clock Fanout Section

