

Tevatron BPM Modes of Operation

Closed Orbit

Description

EchoTek Setup

Timing and Triggering

Post Processing

Data Buffers

Applications

First Turn

Description

EchoTek Setup

Timing and Triggering

Data Buffers

Applications

Turn-by-Turn

Description

EchoTek Setup

Timing and Triggering

Data Buffers

Applications

Diagnostic

Analog Components

Crate Monitor

Trigger & Interrupt Verification

EchoTek Setup

Data Buffer Examination

Applications

Calibration

Electrical Offset

Directivity

Cable Delay

Safe

Description

EchoTek Setup

Timing and Triggering

Data Buffers

Applications

Narrowband Closed Orbit

Description

Post Processing

Data Buffers

Applications

Short-Gate-Sample

Description

EchoTek Setup

FPGA Modification

Timing and Triggering

Data Buffers

Bunch Overlap Profiles

Post Processing

Applications

Very Fast Abort Buffer

Description

EchoTek Setup

Timing and Triggering

Data Buffers

Applications

Wideband DDC Closed Orbit

Description

EchoTek Setup

FPGA Modification

Timing and Triggering

Post Processing

Data Buffers

Applications