MI BPM Project
MI BPM TB Control Module Status Report May 9th, 2006

Significant changes are highlighted.

MI BPM TB Control Module Production

Hardware status:

-) Work in progress on the "Functional Requirements Specification".
-) Conceptual design of the production version of the Control Module completed.
-) Electrical Schematic completed.
-) Parts ordering completed.
-) Printed Circuit Board Layout completed.
-) Print Circuit Boards received.
-) One Module assembled and tested.
-) Other modules Assembly/testing in progress.
Thanks to Tim Kasza for provide some supplemental manpower
(Stew Bledsoe and Rick Mahlum) for the Control Module assembly task.
-) Front Panel design completed.
-) Front Panel Purchase Order placed, manufacturing in progress.

Firmware status:

-) Coding work in progress.

MI BPM TB

Firmware status:

-) Conceptual work in progress together with Control Module (production version) design.

MI BPM TB Control Module Prototype

Hardware status:

-) No activities.

Firmware status (Avnet Xilinx card FPGA):

-) No activities.

Document related to the Control Module are available on the web page:
http://www-ese.fnal.gov/MI_BPM_TB_CTL/

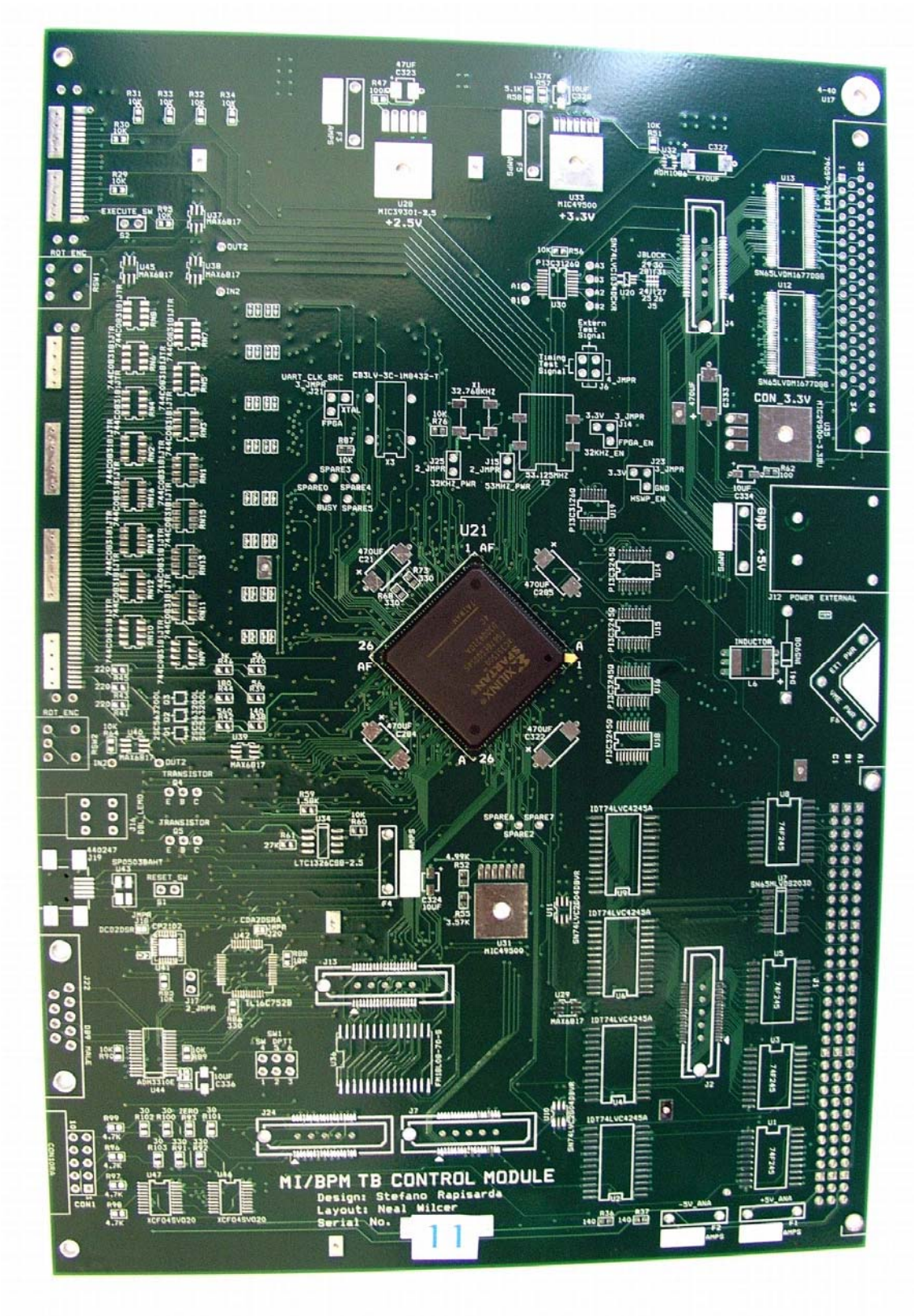
MI BPM Project

MI BPM TB Control Module Time Schedule

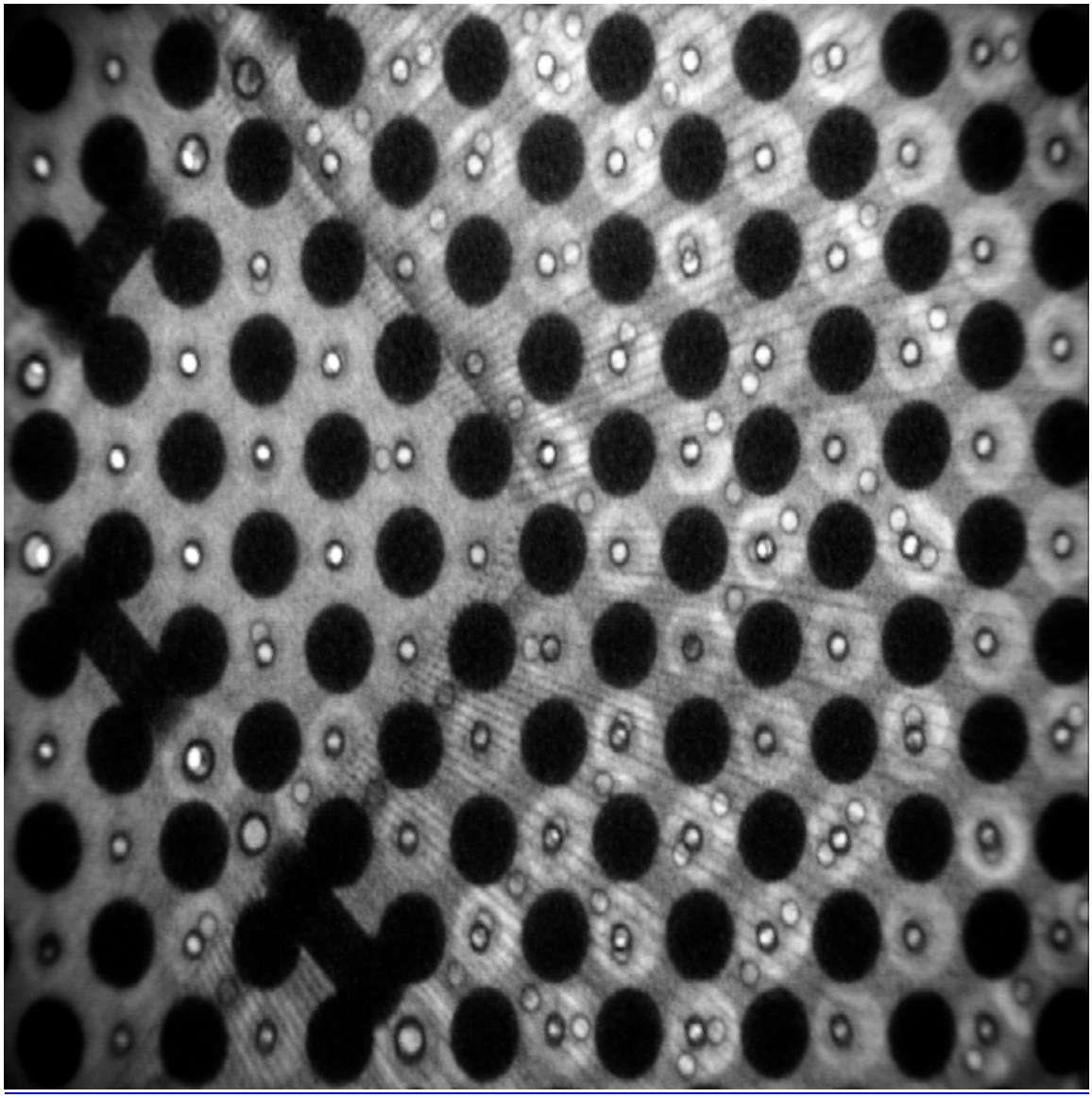
As on February 28th, 2006, last modified on May 9th, 2006

New changes in schedule are highlighted. Previous changes in schedule are in blue.

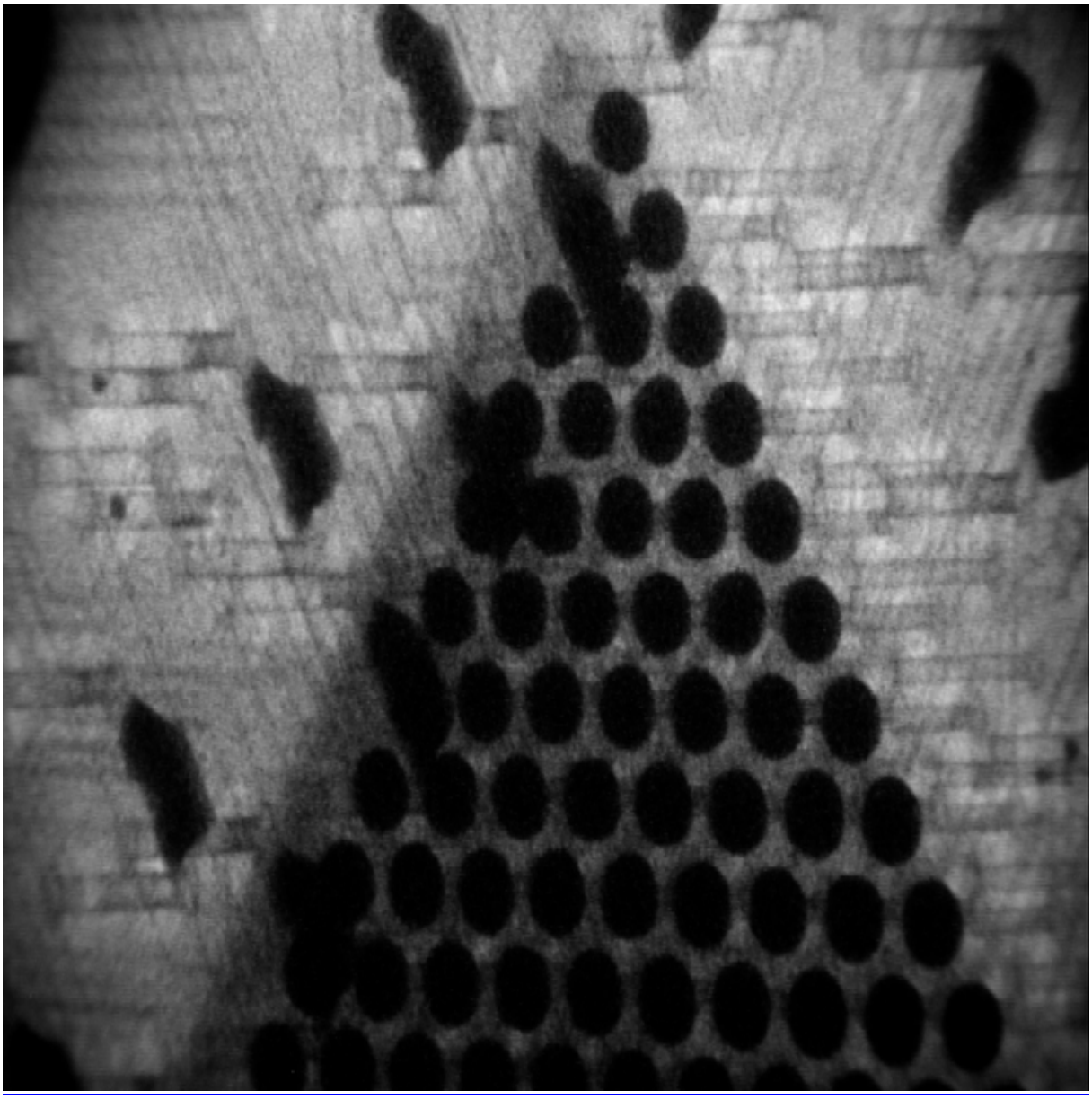
Week beginning on	Task 1	Task 2	Task 3	Task 4	Goals and decisions
February 27th	Design				
March 6th	Design	Schematic			Finalize design
March 13th	Firmware	Schematic			
March 20th	Firmware	Schematic ~~review~~			Minor design changes
March 27th	Firmware	Schematic PCB layout		Parts ordering	
April 3rd	Firmware	Schematic review PCB layout		Parts ordering ~~Parts arrive~~	
April 10th	Firmware	PCB layout	~~PCB quotes~~	Parts arrive	
April 17th	Firmware	PCB layout ~~PCB manufacturing~~	PCB quotes ~~Front panel design~~	Parts arrive	~~Finalize preliminary version of firmware.~~
April 24th	Firmware	PCB Manufacturing Module assembly	~~Front panel manufacturing~~ Front panel design		Finalize preliminary version of firmware. ~~1st Module assembled and tested~~
May 1st		Module assembly	Front panel manufacturing		1st Module assembled and tested May 1st : Does module meets system requirements?
May 8th		Module assembly/ Module testing	Front panel manufacturing		
May 15th		Module testing			May 15th : All Modules assembled and tested
May 22nd					
May 29th					



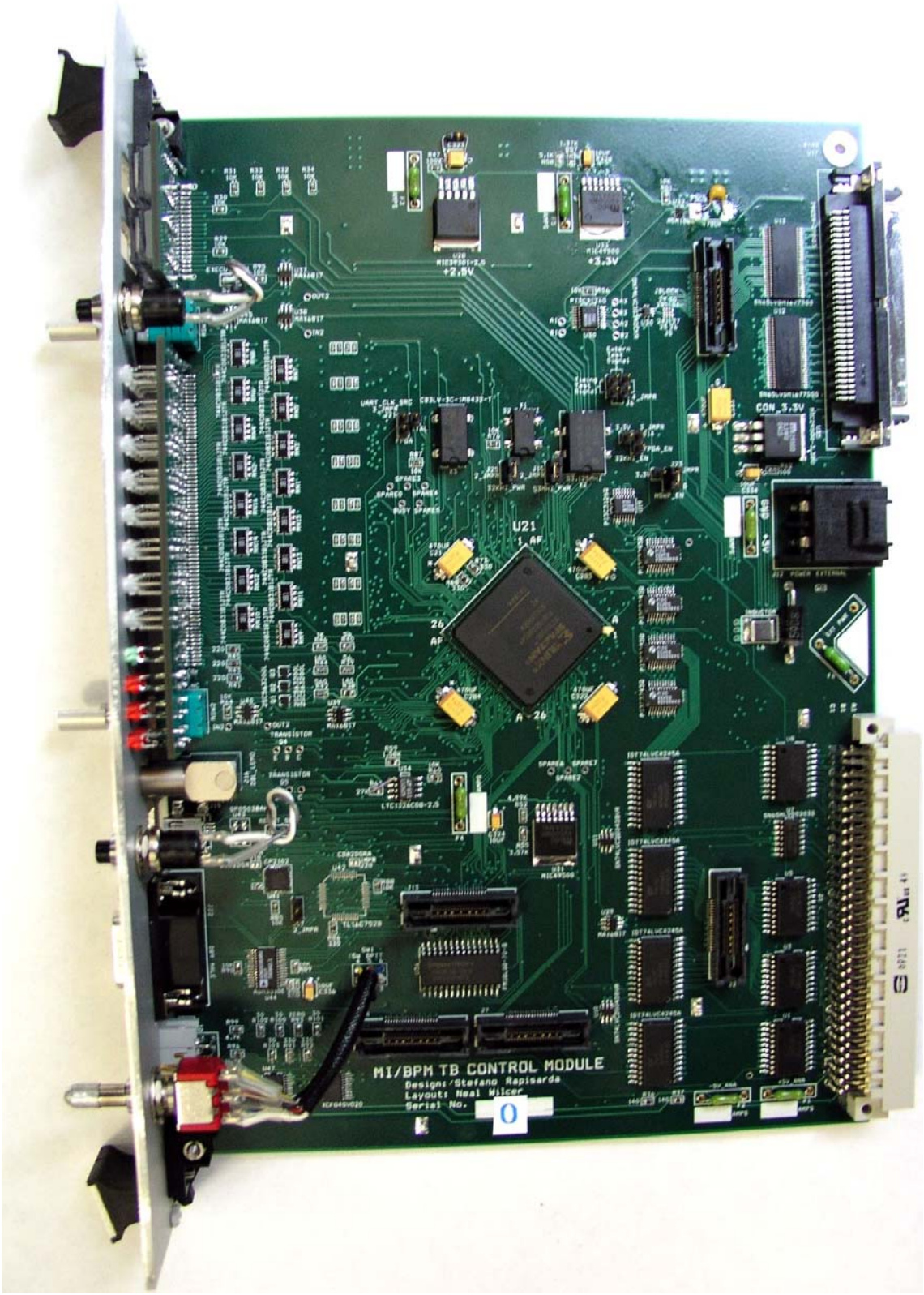
Control Module with FPGA only. FPGA Assembly done by Rick Mahlum.



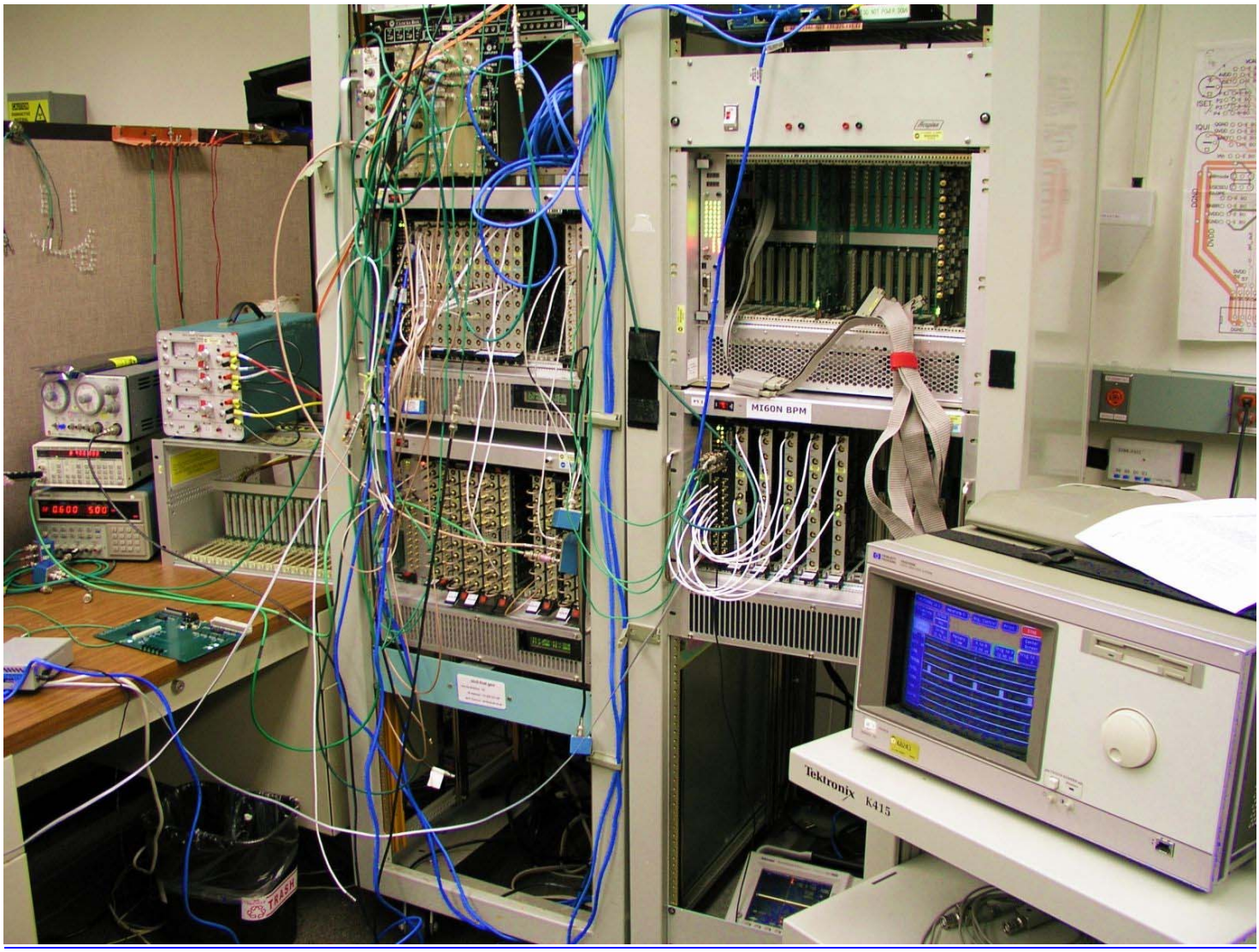
X-Ray of FPGA (center), photo by Jim Franzen.



X-Ray of FPGA (corner), photo by Jim Franzen.



Control Module fully assembled.
Assembly done by Neal Wilcer, Stew Bledsoe and Rick Mahlum.



FCC Test Area