BOOSTER ACCELERATOR LLRF SYSTEM BLOCK DIAGRAM 10/20/2016
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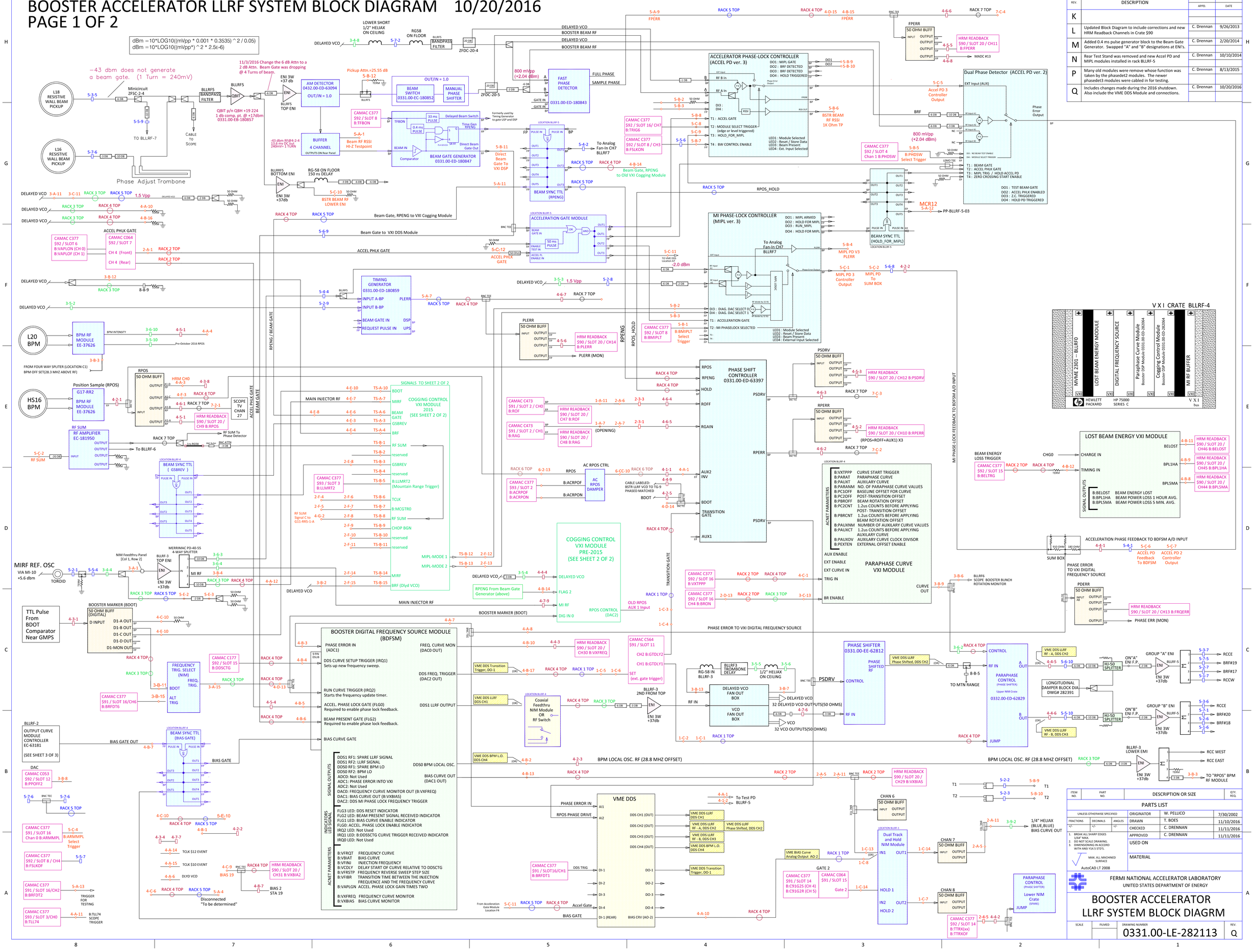
REV.	DESCRIPTION	DRAWN / APPD.	DATE / DATE
K			
L	Updated Block Diagram to include corrections and new HRM Readback Channels in Crate S90	C. Drennan	9/26/2013
M	Added 0.4 ms pulse generator block to the Beam Gate Generator. Swapped "A" and "B" designations at ENI's.	C. Drennan	2/20/2014
N	Rear Test Stand was removed and new Accel PD and MIPL modules installed in rack BLLRF-5	C. Drennan	10/10/2014
P	Many old modules were remove whose function was taken by the phasedet2 modules. The newer phasedet3 modules were cabled in for testing.	C. Drennan	8/13/2015
Q	Includes changes made during the 2016 shutdown. Also include the VME DDS Module and connections.	C. Drennan	10/20/2016



ITEM NO.	PART NO.	DESCRIPTION OR SIZE	QTY. REQ.
PARTS LIST			

UNLESS OTHERWISE SPECIFIED					
FRACTIONS	DECIMALS	ANGLES	ORIGINATOR	W. PELLICO	7/30/2002
+/-	+/-	+/-	DRAWN	T. BOES	11/10/2016
			CHECKED	C. DRENNAN	11/11/2016
			APPROVED	C. DRENNAN	11/11/2016
			USED ON		
			MATERIAL		

1. BREAK ALL SHARP EDGES 1/64" MAX.
2. DO NOT SCALE DRAWING.
3. DIMENSIONING IN ACCORD WITH ANSI Y14.5 STD'S.

MAX. ALL MACHINED SURFACE

AutoCAD LT 2008

FERMI NATIONAL ACCELERATOR LABORATORY
UNITED STATES DEPARTMENT OF ENERGY

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SCALE	FILMED	DRAWING NUMBER	REV.
		0331.00-LE-282113	Q