

Linux Based Embedded Systems

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- A little history
- PC104
- MitySOM Cyclone V (FPGA w/ ARM HPS)
- Embedded Linux
- Real-Time Application Interface (RTAI)

Linux Based Embedded Systems

Embedded Systems at Fermilab in the year 2000

- VME Chassis
- PowerPC based CPU Board
- Various other IO Boards
- VxWorks
- Cost at least \$10K

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The challenge

- Embedded Power Supply controller
 - Should only need power and ethernet. All other cabling is eliminated.
 - Must provide for Control System connectivity
 - < \$1000 for incremental Hardware/Software costs. This will allow for extensive justifiable use.
 - Sixteen 16-bit ADC Channels @ 1Khz per channel
 - Four 16-bit DAC channels
 - 16 bits of Digital I/O for digital status and control
 - Should have small footprint to enable embedding
 - Built-in Transient Recorder to provide PS diagnostics
 - PS Ramp generation functionality possibly to be added.

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The Solution

- PC/104 (ISA) and/or PC/104+ (ISA & PCI)
 - Small formfactor PC – 3.5” x 3.75”
 - Relatively inexpensive – commodity pricing
 - A widely supported standard that still being updated/upgraded
 - Many manufacturers/options.
 - Stackable expansion IO
- x86 based CPU board
- Multi-Function DAQ Board
- Linux OS with RTAI Realtime Extension
- Implemented Front-Ends: PSRAC, BuLB, RBEX, LiLens/Pmag (MC), HWR/SSR1 QPM (PIP2-IT)

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Linac Marx Modulators

- Unique application never done before at Fermilab
- This meant we needed a flexible dev platform to be able try various approaches
- Application demanded an FPGA to do high speed Marx Cell fire commands and real-time feedback
- Lots of IO and high speed state machines needed
- Application also need a embedded CPU to do Modulator pulse learning and interface to ACNET
- High speed FPGA to CPU communication was needed
- Only “soft” real-time processing needed (15Hz)

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Critical Link MitySOM

- Since EES was new to FPGAs, the MitySOM helped us through the FPGA learning curve
- We didn't have to implement everything, just the application specific things
- Critical Link provides documentation and development board design schematics

Critical Link MitySOM-5CSX

Power Supplies

JTAG

1GB DDR3

256MB
DDR3

NOR
Flash

Altera Cyclone V SOC

Cortex-A9
NEON/FPU
L1 Cache

Cortex-A9
NEON/FPU
L1 Cache

L2 CACHE

MMC/
SDIO

I2C
(x4)

GPIO

SPI
(2 master,
2 slave)

10/100/
1000
EMAC (x2)

USB
OTG
(x2)

UART
(x2)

CAN

FPGA Fabric

PCIe
Hard
Core

High
Speed
SerDes

3.125Gbps
Transceivers (x6)

Up to 133 I/O

Card Edge Interface

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Embedded Linux

- Customizable kernel (real-time patches, drivers)
- Minimal footprint – size, robustness
- Network bootable
- Support multiple platforms – x86, ARM, PowerPC
- Minimal disk space requirement
- Maintainable in the long term – OS/Application upgrades and patches

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Embedded Linux

- EES went through several iterations before settling on our current solution
- Looked into SLF: 1.5GB minimum size, x86 only
- TimeSys Linux – embedded Linux with proprietary development environment and real-time OS patches: didn't provide acceptable hard real-time
- Real Time Application Interface (RTAI)
- Linux From Scratch (LFS): customizable, tedious, little or no automation, hard to replicate, not small enough (200-300MB)

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Embedded Linux - Buildroot

- Automated system for building Linux kernels and root filesystems
- Supports many toolchains, target architectures, and hardware platforms
- Integrated system for building and deploying application software
- Supports uClibc and Busybox for reducing the size footprint of our systems
- Supports hundreds of optional software packages
- Creating custom packages (our applications) is supported

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Embedded Linux – Buildroot (cont.)

- Our bzImage files which contain the Linux Kernel, the root filesystem, and Fermi software (application, ACNET FE etc) are ~25MB
- We are able to do network booting using the GRUB or uBoot bootloaders
- Boots into a RAM disk. The local flash disk is only used for node specific information (IP Name/Number etc)
- New Buildroot Development Node (adlinux.fnal.gov) has been recently created (CD VM)

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Real-Time Application Interface (RTAI)

- Provides hard Real-Time (low latency, deterministic interrupts) on Linux systems even if they have only modest hardware
- x86 only (for the most part)
- Requires specific versions of “vanilla” kernels (i.e. from kernel.org)
- In a nutshell, RTAI provides a small RTOS that runs Linux as it's lowest priority task.
- Minimal changes to the Linux kernel allow access to the RTAI API in order to start RTAI tasks and to hook them to interrupts

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Real-Time Application Interface (RTAI)

- Depending on your platform and potential Linux Kernel version limitations, it is now possible that a properly configured Linux kernel with the latest PREEMPT-RT patches makes the use of RTAI unnecessary.
- In other words Linux can be (depending on your definition and your application) hard real-time.